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(71) Applicant (for all designated States except US): ROSE-HULMAN INSTITUTE OF TECHNOLOGY [US/US]; 5500 Wabash Avenue, Terre Haute, IN 47803 (US).

(72) Inventors; and

(75) Inventors/Applicants (for US only): MOHAMMAD, R., Sayeh [US/US]; 38 Lake Indian Hills Ridge, Carbondale, IL 62902 (US). AZAD, Siahmakoun [US/US]; 126 Lakeshore, Terre Haute, IN 47803 (US).

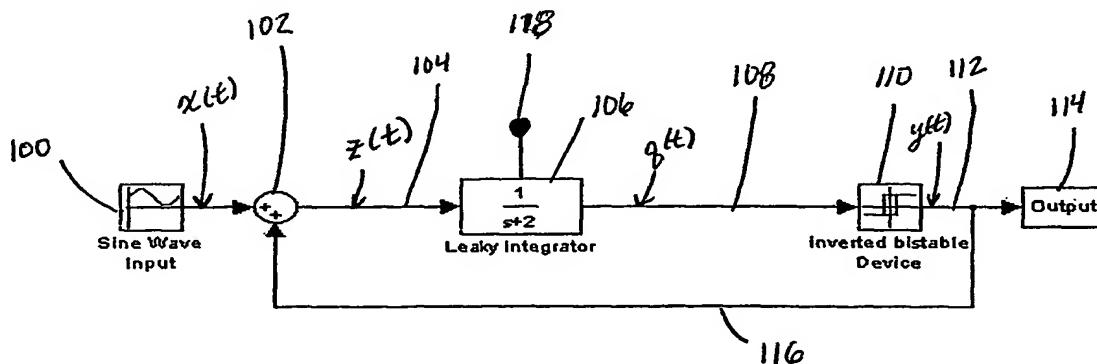
(74) Agent: ORICH, Christine, E.M.; Bose McKinney & Evans LLP, 2700 First Indiana Plaza, 135 North Pennsylvania Street, Indianapolis, IN 46204 (US).

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(54) Title: BINARY DELTA-SIGMA MODULATOR



(57) Abstract: A modulator, including an integrator (118), a bistable device (110), an adder (102) and a feedback loop (116) is provided. A method for converting a continuous time signal to a binary signal is also provided.

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